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Expedited Procedure
Technology Center 2800****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Serial No.:	10/602,369	Conf. No.:	1172
Filing Date:	06/24/2003	Art Unit:	2825
Applicant:	Dotson <i>et al.</i>	Examiner:	To, Tuyen P.
Title:	SYSTEM, METHOD AND PROGRAM PRODUCT FOR POSITIONING I/O PADS ON A CHIP	Docket No.:	END920030008US1 (IBME-0077)

COMMISSIONER FOR PATENTS**DESTINATION FACSIMILE NUMBER:** 571-273-8300


Transmitted herewith is: **After Final Amendment in 12 pages and
Exhibit A in 2 pages**

in the above identified application.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date shown below.

Hunter E. Webb
(Person transmitting this correspondence)



Signature

February 28, 2006

Date

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AFTER-FINAL AMENDMENT

Sir:

I. INTRODUCTORY COMMENTS:

This paper is being filed in response to the Final Office Action dated December 28, 2005.

Please amend the above-referenced patent application as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks begin on page 12 of this paper.

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